

Substitute Form PTO-1449 (Modified)  <b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-317001	Application No. 09/760,509
	Applicant Gilbert Wolrich et al.		
	Filing Date January 12, 2001	Group Art Unit 2183	

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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	AS	5,189,636	02/1993	Patti et al.			
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	AV	5,247,671	09/1993	Adkins et al.			
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	AY	5,428,779	06/1995	Allegrucci et al.			
↓	AZ	5,463,746	10/1995	Brodnax et al.			
AL	BA	5,541,920	07/1996	Angle et al.			

Examiner Signature /Aimee Li/	Date Considered 03/05/2007
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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AL	CB	5,937,177	08/1999	Molnar et al.			

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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
AL	DV	0 130 381	01/1985	Europe				
	DW	0 463 855	01/1992	Europe				
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	DY	0 696 772	02/1996	Europe				
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AL	EA	WO03/019399	03/2003	WIPO				

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Foreign Patent Documents or Published Foreign Patent Applications								
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							Yes	No
AL	EB	WO03/085517	10/2003	WIPO				

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	EI	Hirata, H., et al., "An Elementary Processor Architecture with Simultaneous Instruction Issuing from Multiple Threads", <i>Proc. 19th Annual International Symposium on Computer Architecture</i> , ACM & IEEE-CS, 20(2):136-145, May 1992.
	EJ	Intel, "1A-64 Application Developer's Architecture Guide," Rev.1.0, pp. 2-2, 2-3, 3-1, 7-165, C-4, and C-23, May 1999.
	EK	Koch, G., et al., "Breakpoints and Breakpoint Detection in Source Level Emulation", <i>Proc. 19th International Symposium on System Synthesis (ISSS '96)</i> , <i>IEEE</i> , pp. 26-31 (1996).
	EL	Moors, et al., "Cascading Content-Addressable Memories", <i>IEEE Micro</i> , 12(3):56-66 (1992).
↓	EM	Okamoto, K., et al., "Multithread Execution Mechanisms on RICA-1 for Massively Parallel Computation", <i>IEEE - Proc. Of PACT '96</i> , pp. 116-121 (1996).
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AL	ES	Ramsey, N., "Correctness of Trap-Based Breakpoint Implementations", <i>Proc. of the 21st ACM Symposium on the Principles of Programming Languages</i> , pp. 15-24 (1994).

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Other Documents (include Author, Title, Date, and Place of Publication)		
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# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

METHOD AND APPARATUS FOR PROVIDING LARGE  
REGISTER ADDRESS SPACE WHILE MAXIMIZING CYCLETIME  
PERFORMANCE FOR A MULTI-THREADED REGISTER FILE SET

Application Number : 09/760509



Confirmation Number: 2157

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-317001

Art Unit: 2183

Examiner: Barry J. OBrien

Search string: ( 6223238 or 6223279 or 6247025 or 6256713 or 6272616 or 6275505 or 6279113 or 6289011 or 6298370 or 6307789 or 6324624 or 6345334 or 6347344 or 6351808 or 6356962 or 6373848 or 6360262 or 6389449 or 6393483 or 6415338 or 6426940 or 6427196 or 6430626 or 6434145 or 6463072 or 6532509 or 6552826 or 6560667 or 6577542 or 6584522 or 6587906 or 6606704 or 6625654 or 6629237 or 6631430 or 6631462 or 6661794 or 6667920 or 6668317 or 6681300 or 6694380 or 4023023 or 5113516 or 5363448 or 5436626 or 5600812 or 5652583 or 5717760 or 4992934 or 5450603 ).pn

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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	2	6223279	2001-04-24	Nishimura et al.			
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**Signature**

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or 5689566 or 5692167 or 5699537 or 5717898 or 5721870 or 5742587 or 5742782  
or 5742822 or 5745913 or 5751987 or 5761507 or 5761522 or 5781774 or 5784649  
or 5784712 or 5796413 or 5797043 or 5809235 or 5809530 or 5812839 or 5812868  
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or 5887134 or 5890208 or 5892979 or 5905876 or 5905889 or 5915123 or 5933627  
or 5659687 ).pn

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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	2	5574922	1996-11-12	James			
	3	5574939	1996-11-12	Keckler et al.			
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	12	5649157	1997-07-15	Williams			
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	14	5680641	1997-10-21	Sidman			
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✓	16	5692167	1997-11-25	Grochowski et al.			
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AL	18	5717898	1998-02-10	Kagan et al.			
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	20	5742587	1998-04-21	Zornig et al.			
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Signature

Examiner Name	Date
/Aimee Li/	03/05/2007

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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## Title of Invention

METHOD AND APPARATUS FOR PROVIDING LARGE  
REGISTER ADDRESS SPACE WHILE MAXIMIZING CYCLETIME  
PERFORMANCE FOR A MULTI-THREADED REGISTER FILE SET

Application Number : 09/760509



Confirmation Number: 2157

First Named Applicant: Gilbert Wolrich

Attorney Docket Number: 10559-317001

Art Unit: 2183

Examiner: Barry J. OBrien

Search string: ( 3373408 or 3478322 or 3792441 or 3913074 or 3940745 or 4130890 or 4392758 or 4400770 or 4514807 or 4523272 or 4724521 or 4745544 or 4777587 or 4866664 or 5073864 or 5140685 or 5142683 or 5155831 or 5155854 or 5168555 or 5173897 or 5255239 or 5263169 or 5347648 or 5357617 or 5367678 or 5390329 or 5392391 or 5392411 or 5392412 or 5404464 or 5404482 or 5428809 or 5432918 or 5442756 or 5448702 or 5450351 or 5452437 or 5459842 or 5463625 or 5467452 or 5481683 or 5487159 or 5517628 or 5517648 or 5542070 or 5542088 or 5544236 or 5550816 or 5557766 or 20020053017 or 20020056037 or 20030041228 or 20030145159 or 20030191866 or 20040039895 or 20040054880 or 20040071152 or 20040073728 or 20040073778 or 20040098496 or 20040109369 or 20040205747 ).pn

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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AL	2	20020056037	2002-05-09	Wolrich et al.			
AL	3	20030041228	2003-02-27	Rosenbluth et al.			



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✓	12	20040109369	2004-06-10	Wolrich et al.			
AL	13	20040205747	2004-10-14	Bernstein et al.			

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